

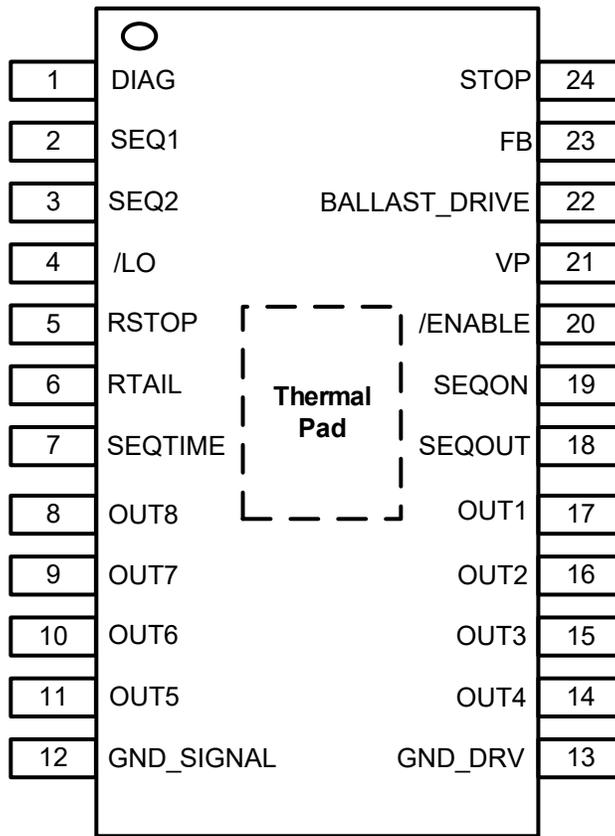


## ORDER INFORMATION

Valid Part Number	Package Type	Top Code
PT16974-HX	24 Pins, HSSOP	PT16974-HX

## PIN CONFIGURATION

Top View



## PIN DESCRIPTION

Pin Name	Description	Pin No.
DIAG	Diagnostic output. Requires a pull-up resistor. Normal operation in logic low, and indicate fault status in logic high. Fault status includes LED open circuit, RSTOP current limit, set back current Limit down 20%, and thermal shutdown. Forcing this pin below 1.8V (typ) to reinitiate the device. Ground this pin if not used.	1
SEQ1	Output sequencing setting. Ground or leave this pin floating for sequencing programming.	2
SEQ2	Output sequencing setting. Ground or leave this pin floating for sequencing programming.	3
$\overline{\text{LO}}$	Ground this pin for latch off function.	4
RSTOP	STOP current programming pin. Connect a resistor to GND_Signal to set the output current of STOP mode.	5
RTAIL	TAIL current PWM duty cycle programming pin. Connect a resistor to GND_Signal to set the dimming duty cycle. Ground pin if using external modulation.	6
SEQTIME	Sequence Time programming pin. Connect a resistor to GND_Signal to set the sequence time.	7
OUT8	Channel 8 constant current output to LED. Ground this pin to GND_DRV is not used.	8
OUT7	Channel 7 constant current output to LED. Ground this pin to GND_DRV is not used.	9
OUT6	Channel 6 constant current output to LED. Ground this pin to GND_DRV is not used.	10
OUT5	Channel 5 constant current output to LED. Ground this pin to GND_DRV is not used.	11
GND_Signal	Analog ground. Return for the internal reference voltage and analog circuit. Connect to circuit ground, GND, to complete return path.	12
GND_DRV	Power ground. Return for high current (8 channels). Connect to circuit ground, GND, to complete return path.	13
OUT4	Channel 4 constant current output to LED. Ground this pin to GND_DRV is not used.	14
OUT3	Channel 3 constant current output to LED. Ground this pin to GND_DRV is not used.	15
OUT2	Channel 2 constant current output to LED. Ground this pin to GND_DRV is not used.	16
OUT1	Channel 1 constant current output to LED. Ground this pin to GND_DRV is not used.	17
SEQOUT	Sequence status reporting, open-drain output, requires a pull-up resistor. Follows $\overline{\text{ENABLE}}$ pin after delay of OUT8 with SEQON high.	18
SEQON	Sequence mode enable. Requires a pull-up resistor, tie this pin to logic high, turns on 1-8 output sequencing.	19
$\overline{\text{ENABLE}}$	Global enable input. Ground this pin to turn device on.	20
VP	Supply voltage input.	21
Ballast Drive	Gate drive for external power distribution PFET. Ground if not used.	22
FB	Feedback Sense reference, connect resistor divider from the top (anode) of the LED strings (Vstring) to set the regulation voltage. Connect to GND if not use.	23
STOP	Stop Logic Input. External Modulation Input when VP is high.	24
Thermal pad	Ground. Do not connect to PCB traces other than GND.	Thermal pad

## **IMPORTANT NOTICE**

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