

DESCRIPTION

The RS1117A is a 1A low-dropout positive voltage regulator. It is available in fixed and adjustable output voltage versions. Over current and thermal protection are integrated onto the chip. Output current will limit as while it reaches the pre-set current or temperature limit. The dropout voltage is specified at 1.40V Maximum at full rated output current. The RS1117A series provides excellent regulation over line, load and temperature variations.

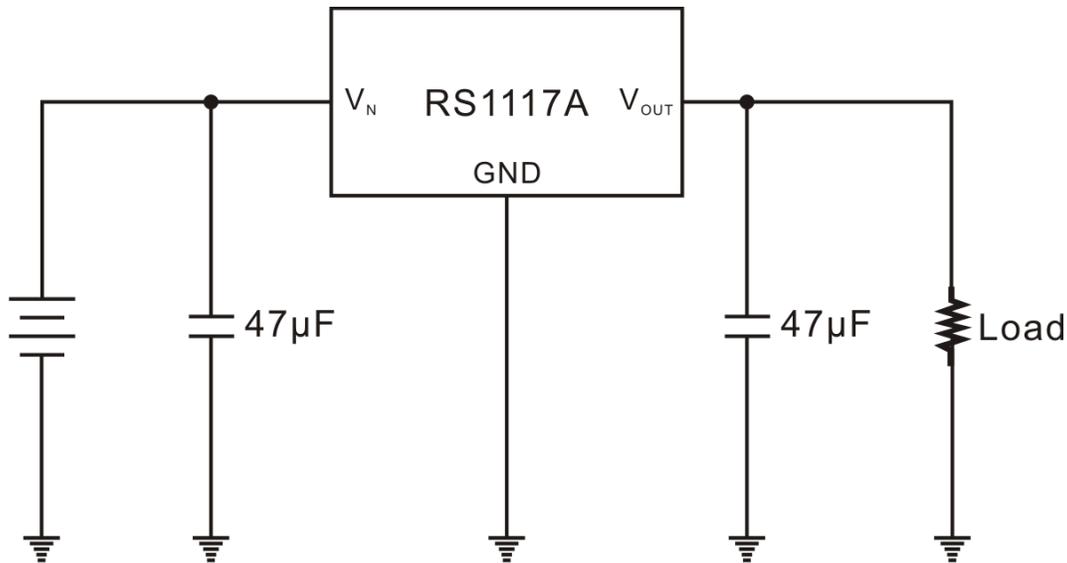
FEATURES

- Low Dropout Voltage 1.40V (Max.) at 1.0A
- Adjustable or Fixed Voltage (1.8V, 2.5V, 3.3V, 5.0V)
- Over Current Protection
- Thermal Overload Protection
- Maximum Line Regulation 0.2%
- Maximum Load Regulation 1.0%
- Adjust Pin Current Less Than 120 μ A

APPLICATIONS

- SCSI-2 Active Termination
- High Efficiency Linear Regulators
- 5V to 3.3V Voltage Converter
- Battery Charger
- Battery Management Circuits for Notebook and Palmtop PCs
- Core Voltage Supply: FPGA, PLD, DSP, CPU

APPLICATION CIRCUIT

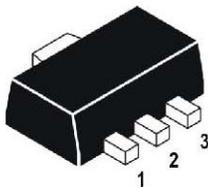


ORDER INFORMATION

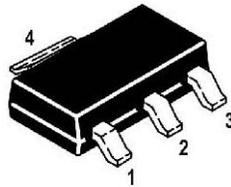
Device	Device Code
RS1117A-XX YY Z	<p>XX is package designator: M: SOT-89 SJ: SOT-223 J: TO-252</p> <p>YY is nominal output voltage designator: AD: ADJ 18: 1.8V 25: 2.5V 33: 3.3V 50: 5.0V</p> <p>Z is Lead Free designator: P: Commercial Standard, Lead (Pb) Free and Phosphorous (P) Free Package G: Green (Halogen Free with Commercial Standard)</p>

PIN ASSIGNMENTS

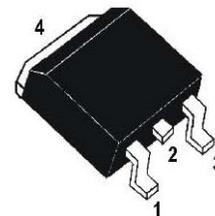
SOT-89



SOT-223



TO-252



PIN DESCRIPTION

Package	Pin No.	Pin Name	Description
SOT-89	1	ADJ/GND	Ground Pin or ADJ Terminal Pin for Adjustable.
	2	VOUT	Regulator Output Pin
	3	VIN	Regulator Input Pin
SOT-223	1	ADJ/GND	Ground Pin or ADJ Terminal Pin for Adjustable.
	2, 4	VOUT	Regulator Output Pin
	3	VIN	Regulator Input Pin
TO-252	1	ADJ/GND	Ground Pin or ADJ Terminal Pin for Adjustable.
	2, 4	VOUT	Regulator Output Pin
	3	VIN	Regulator Input Pin

IMPORTANT NOTICE

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PTC cannot assume responsibility for use of any circuitry other than circuitry entirely embodied in a PTC product. No circuit patent licenses are implied.

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