

## FEATURES

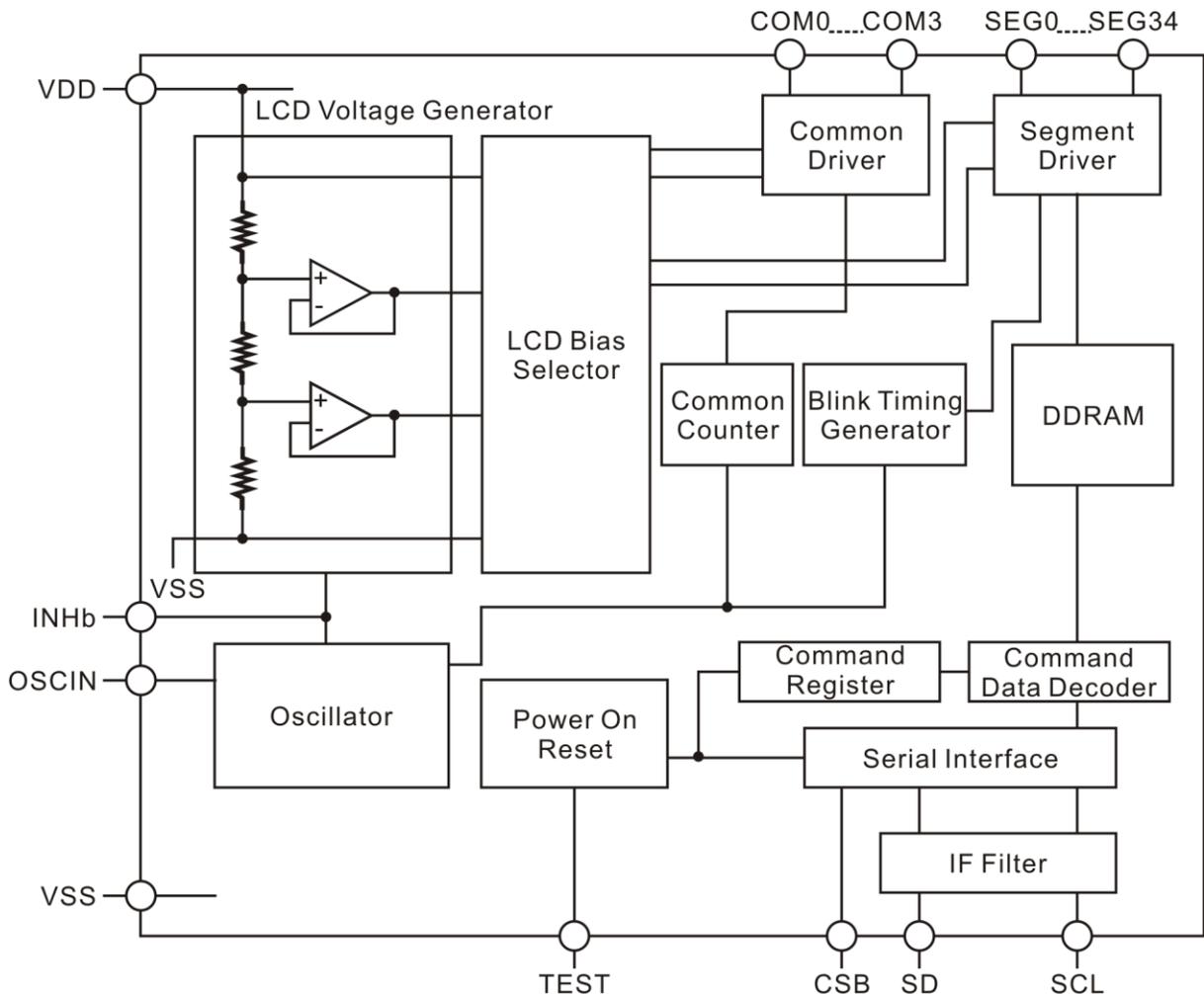
- 3-wire serial interface (SD, SCL, CSB)
- Integrated RAM for display data (DDRAM): 35 x 4 bits (Max. 140 Segment)  
LCD driving port: 4 Common output, 35 Segment output
- Integrated Buffer AMP for LCD driving power supply
- Integrated Oscillator Circuit
- Support Blink Function (Blink Function 0.5, 1, 2Hz selection)
- Integrated Power-On-Reset Circuit
- Display Duty: 1/4

- 1/2 bias, 1/3 bias selectable
- No external components
- Low power/ Ultra low power consumption design
- Operating power supply: 2.7 ~ 3.6V
- Package: 48-pin, LQFP

## APPLICATIONS

- Telephone, FAX, Portable equipment (POS, ECR, PDA etc.)
- DSC, DVC, Car audio, Home electrical appliance, Master equipment, etc

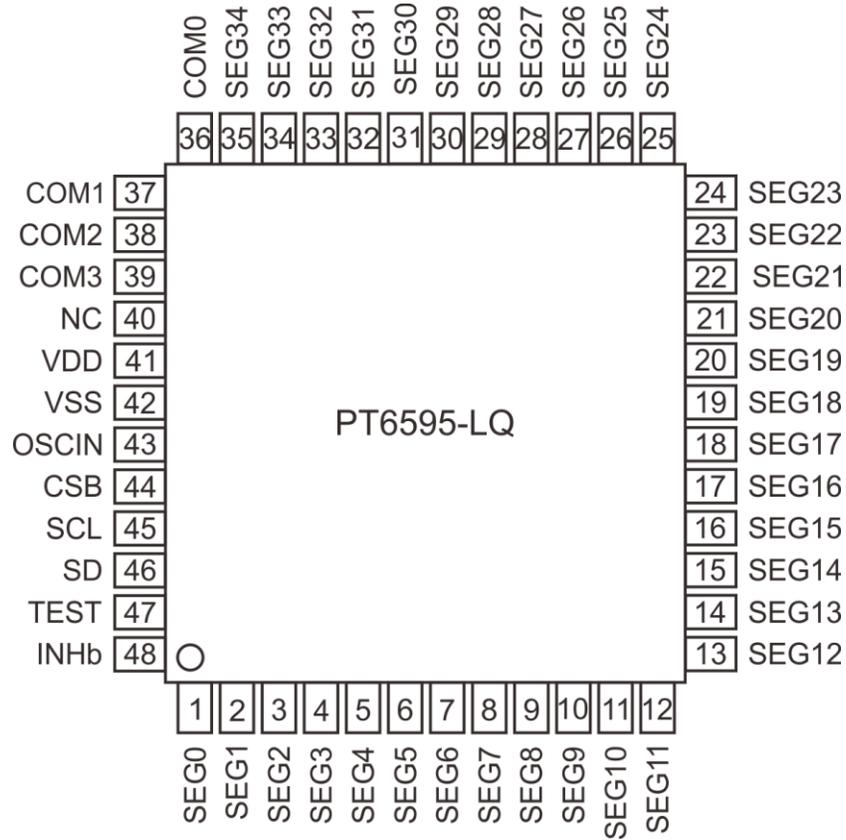
## BLOCK DIAGRAM



## ORDER INFORMATION

Valid Part Number	Package Type	Top Code
PT6595-LQ	48 Pins, LQFP	PT6595-LQ

## PIN CONFIGURATION



## PIN DESCRIPTION

Pin Name	I/O	Description	Pin No.
SEG0 ~ SEG34	O	Segment output for LCD driver	1 ~ 35
COM0 ~ 3	O	Common output for LCD driver	36 ~ 39
NC	-	No connection	40
VDD	-	Power supply	41
VSS	-	Ground pin	42
OSCIN	I	External clock input External clock and Internal clock can be selected by command. Must be connect to VSS when use internal oscillation circuit.	43
CSB	I	Chip select: "L" active	44
SCL	I	Serial data transfer clock	45
SD	I	Serial data input	46
TEST	I	Test input (Testing Use) Must be connect to VSS	47
INHb	I	Input terminal for turn off display H: turn on display L: turn off display	48

## **IMPORTANT NOTICE**

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