

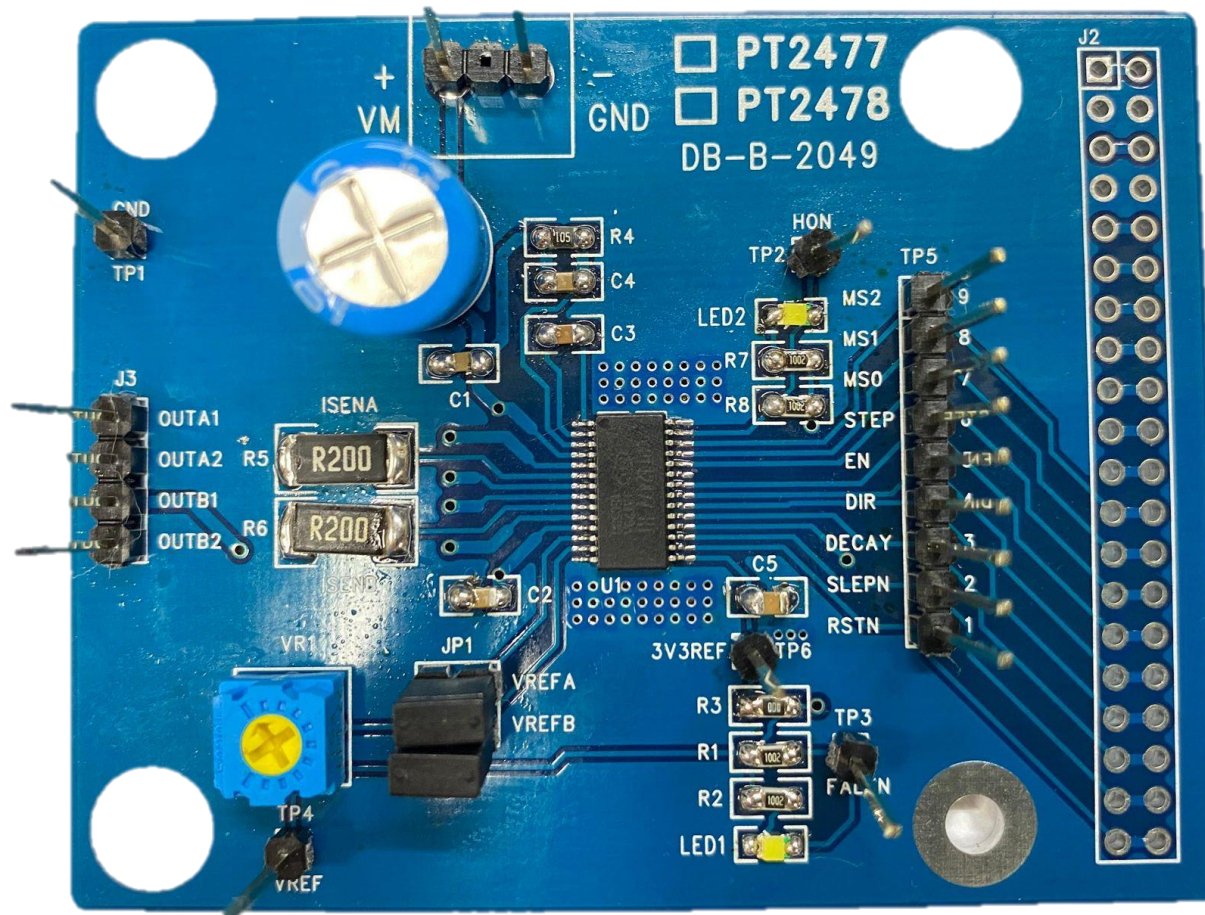
PT12477 Demo Board Operation

2023/03/08

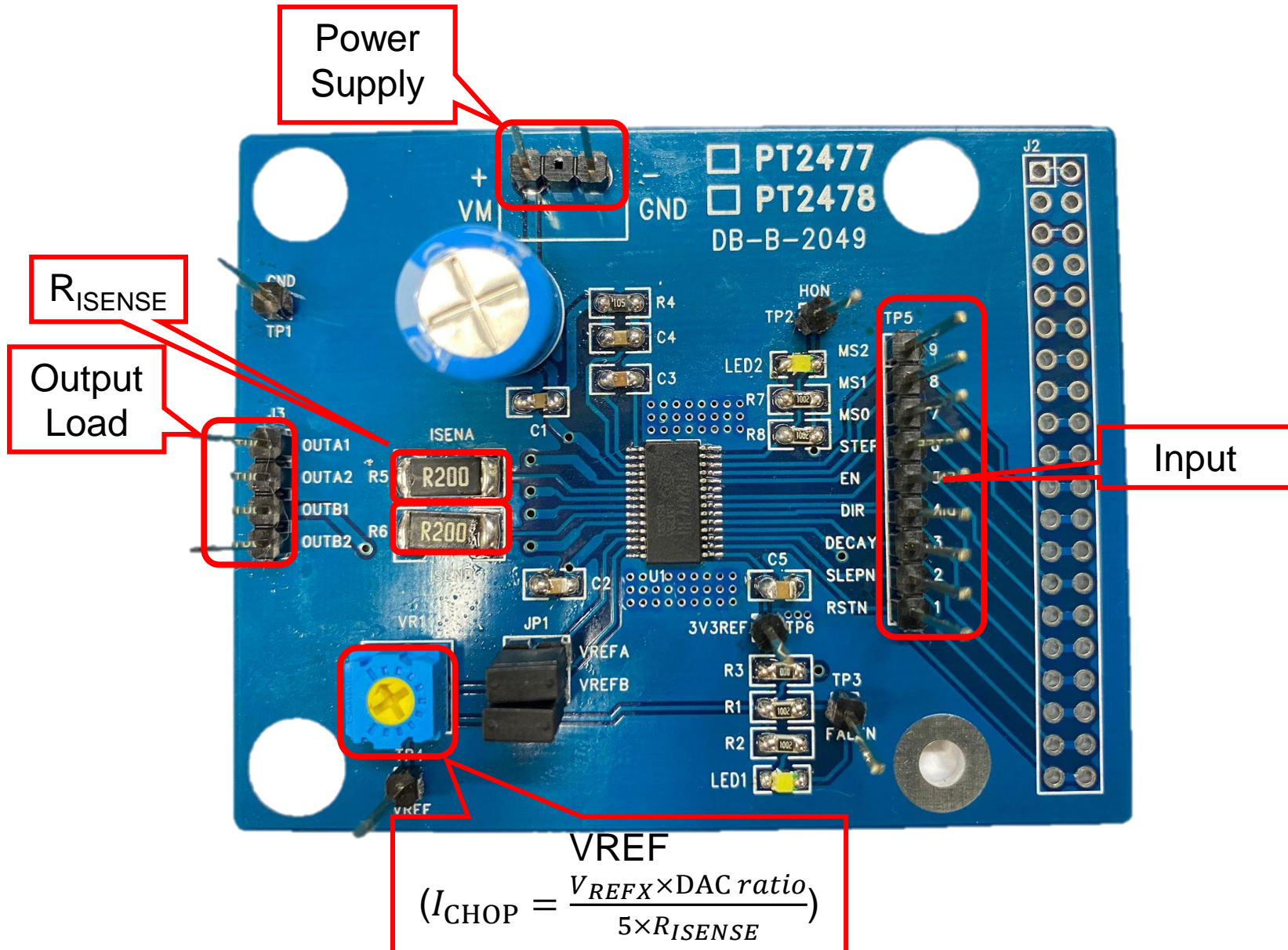
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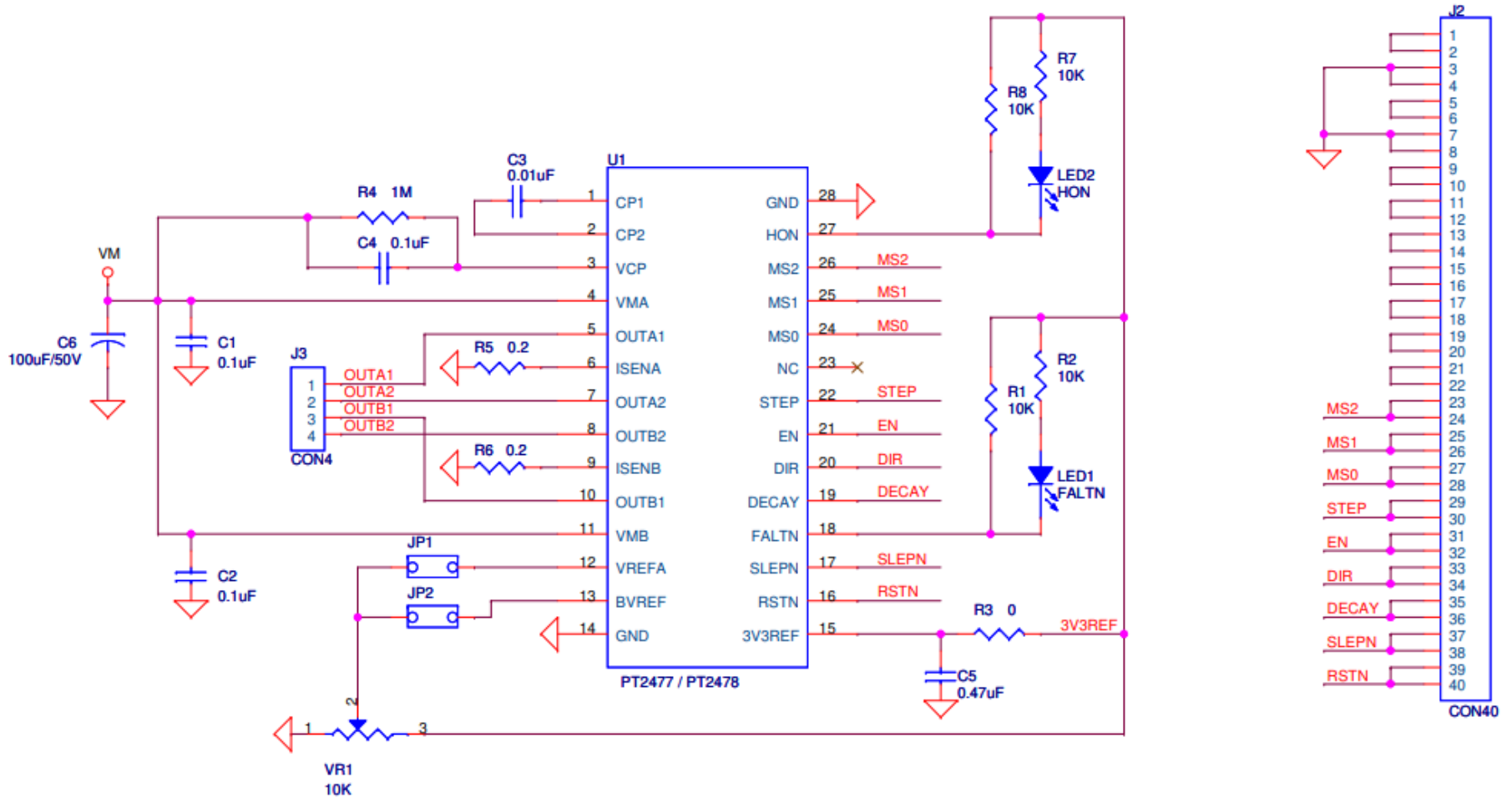
PT12477 EV Board Outline



PT12477 EV Board I/O Port Description



PT12477 EV Board Circuit



PT12477 EV Board Input Pins Setting

Pin Name	Description	Pin No. HTSSOP
RSTN	Reset input. Logic L=Initialize all of internal logic registers and disables H-bridge outputs, Logic H= normal operation.	16
SLEPN	Sleep mode input Logic H=device enable, Logic L=low-power sleep mode.	17
DECAY	Decay mode input Logic L=slow decay, Open=mixed decay, Logic H=fast decay	19
DIR	Motor rotation Direction logic input (with Internal pulldown). Logic H= winding current A leading winding current B Logic L= winding current B leading winding current A	20
EN	Enable input (with Internal pulldown) Logic H = disable device outputs and sequencer operation Logic L = enable	21
STEP	Step input (with Internal pulldown). The step sequencer moves to next step during rising edge of STEP clock input.	22
MS0	Micro-stepping sequencer configurations (with internal pull-down), Full, 1/2, 1/4, 1/8, 1/16, or 1/32 step depends combination of MS pins.	24
MS1		25
MS2		26

PT12477 EV Board Input Pins Setting

MS2	MS1	MS0	STEP MODE
0	0	0	Full step (2-phase excitation) with 71% of full-scale current
0	0	1	1/2 step (1-2 phase excitation)
0	1	0	1/4 step (W1-2 phase excitation)
0	1	1	1/8 microstep (2W1-2 phase excitation)
1	0	0	1/16 microstep (4W1-2 phase excitation)
1	0	1	1/32 microstep
1	1	0	1/32 microstep
1	1	1	1/32 microstep

Decay Pin Level	DECAY Pin Logic	Decay Mode
<0.6V	L	Slow
OPEN	FLOAT	Mixed
>2V	H	Fast