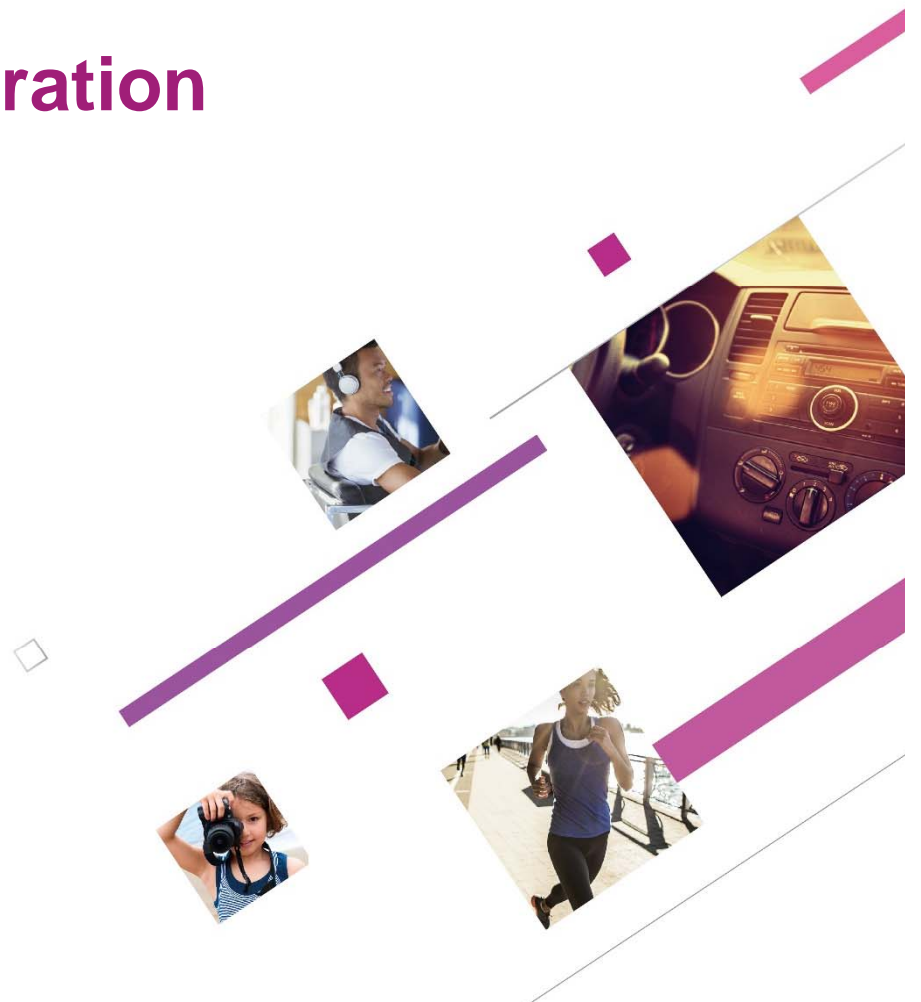


PT2465 Demo Board Operation

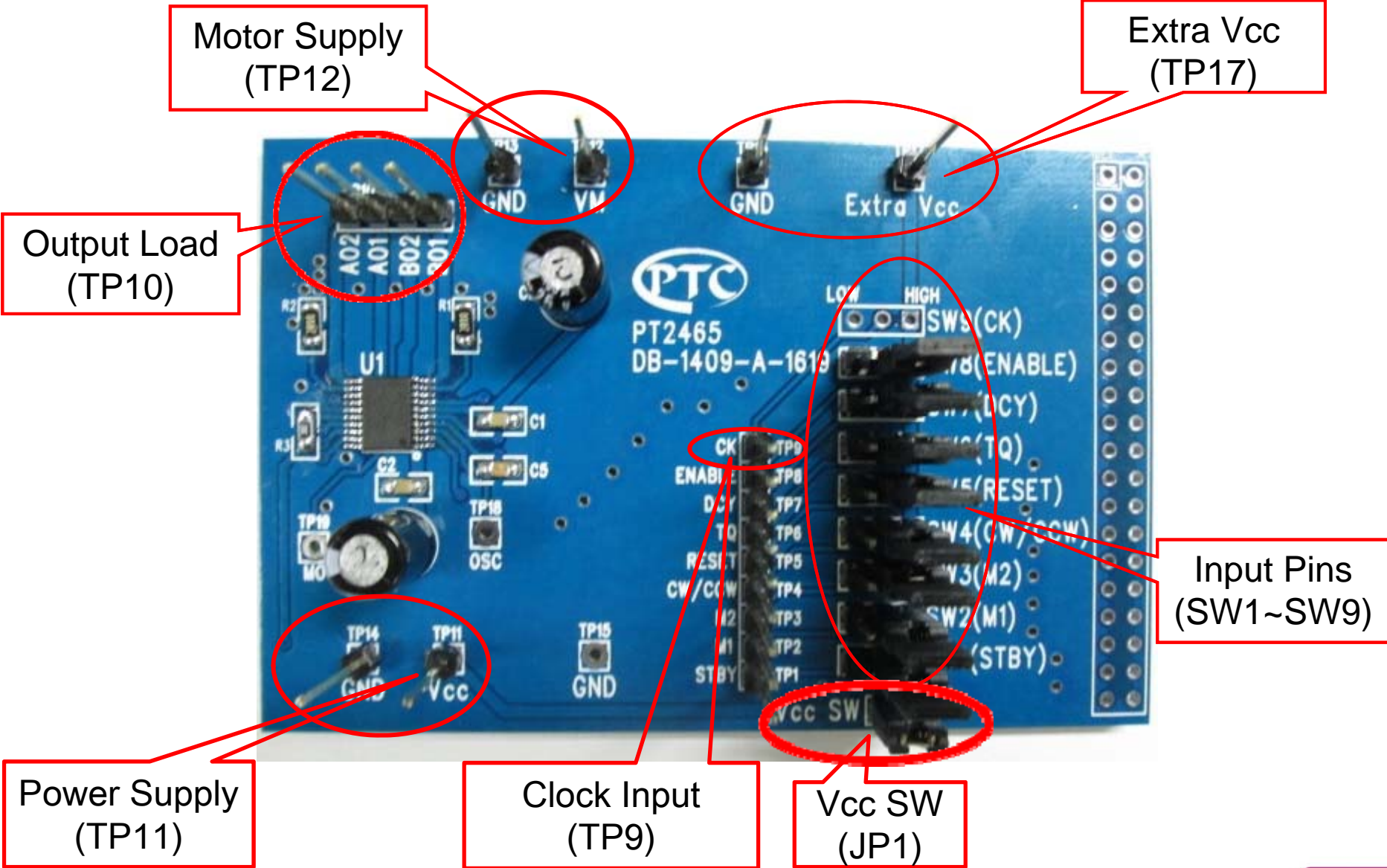
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PT2465 EV Board Outline

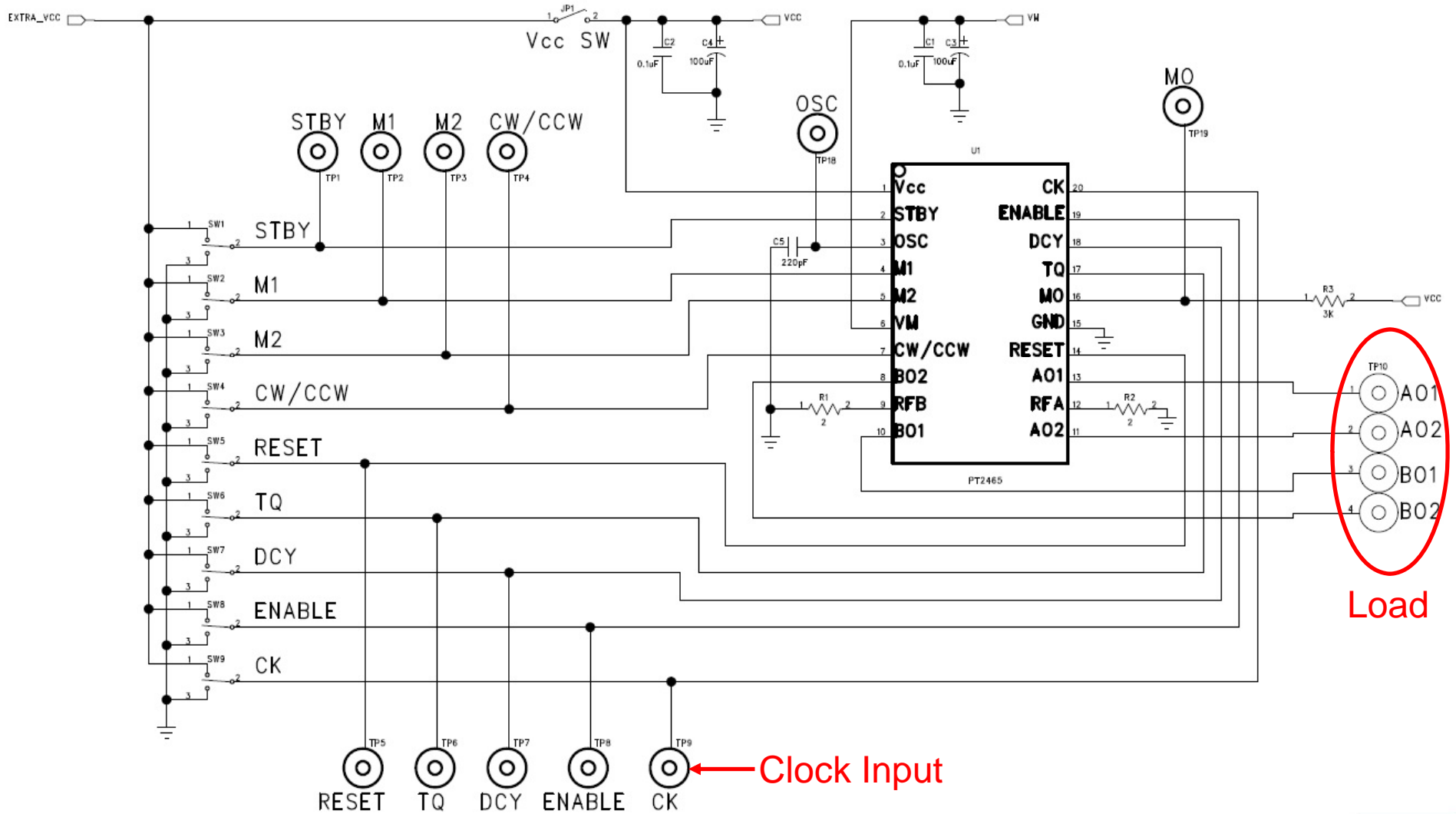


PT2465 EV Board I/O Port Description



PT2465 EV Board Circuit

Vcc SW(JP1) ON : The input pins(SW1~SW9) high voltage are Vcc.
Vcc SW(JP1) OFF : The input pins(SW1~SW9) high voltage are Extra Vcc.



PT2465 EV Board Input Pins Setting (1)

INPUT SIGNALS AND OPERATION MODES

Inputs					Operation Mode
CK	CW/CCW	RESET	ENABLE	STBY	
	L	H	H	H	CW
	H	H	H	H	CCW
X	X	L	H	H	Initial Mode
X	X	X	L	H	Enable Wait mode (Outputs: high impedance)
X	X	X	X	L	Standby mode (outputs: high impedance)

EXCITATION MODE SETTINGS

Inputs		Excitation Mode
M1	M2	
L	L	2-phase
H	L	1-2-phase
L	H	W1-2-phase
H	H	2W1-2-phase

VREF VOLTAGE SETTING

Input	Vref
TQ	
L	0.125 V
H	0.5 V

PT2465 EV Board Input Pins Setting (2)

SETTING THE CURRENT DECAY MODE

TABLE. FAST-DECAY TIME INSERTED DURING THE CURRENT DECAY PERIOD

which is expressed as the number of CK cycles (an actual value may not exactly equal to the specified value)

Input	2W 1-2phase			W1-2phase			1-2phase		
	Predefined Current	Number of CK Cycles		Predefined Current	Number of CK Cycles		Predefined Current	Number of CK Cycles	
DCY	%	TQ=H	TQ=L	%	TQ=H	TQ=L	%	TQ=H	TQ=L
L	100			100			100		
	98	0	0						
	92	0	0	92	0	0			
	83	0	0						
	71	0	0	71	0	0	71	0	0
	56	0	0						
	38	0	0	38	0	0			
	20	0	0						
H	0	0	0	0	0	0	0	0	0
	100			100			100		
	98	2	1						
	92	2	1	92	2	1			
	83	2	1						
	71	2	1	71	4	2	71	4	2
	56	4	2						
	38	4	2	38	4	2			
20	4	2							
0	0	0	0	0	0	0	0	0	