

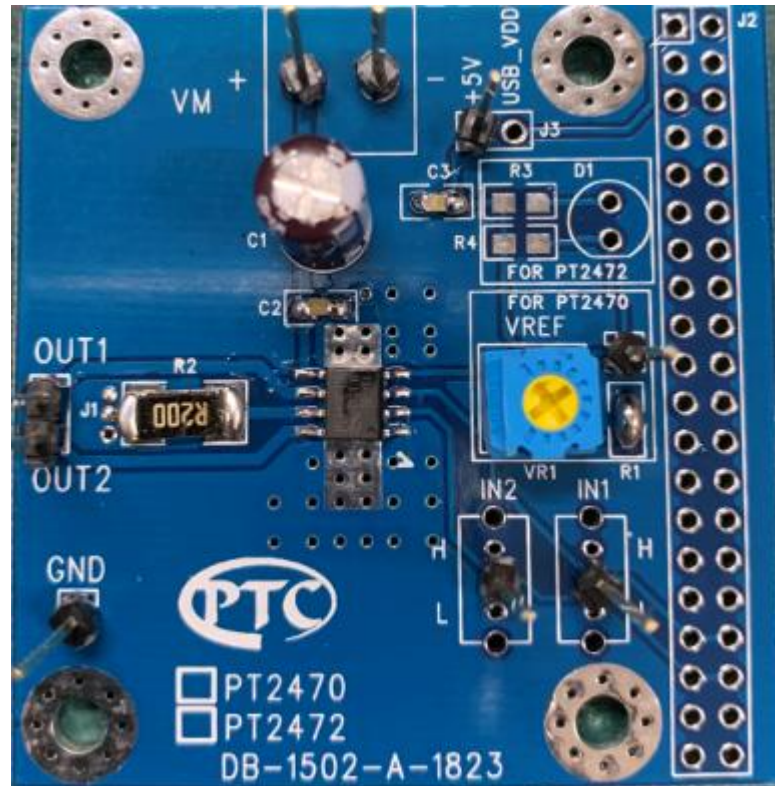
PT2470 Demo Board Operation

2019/05/29

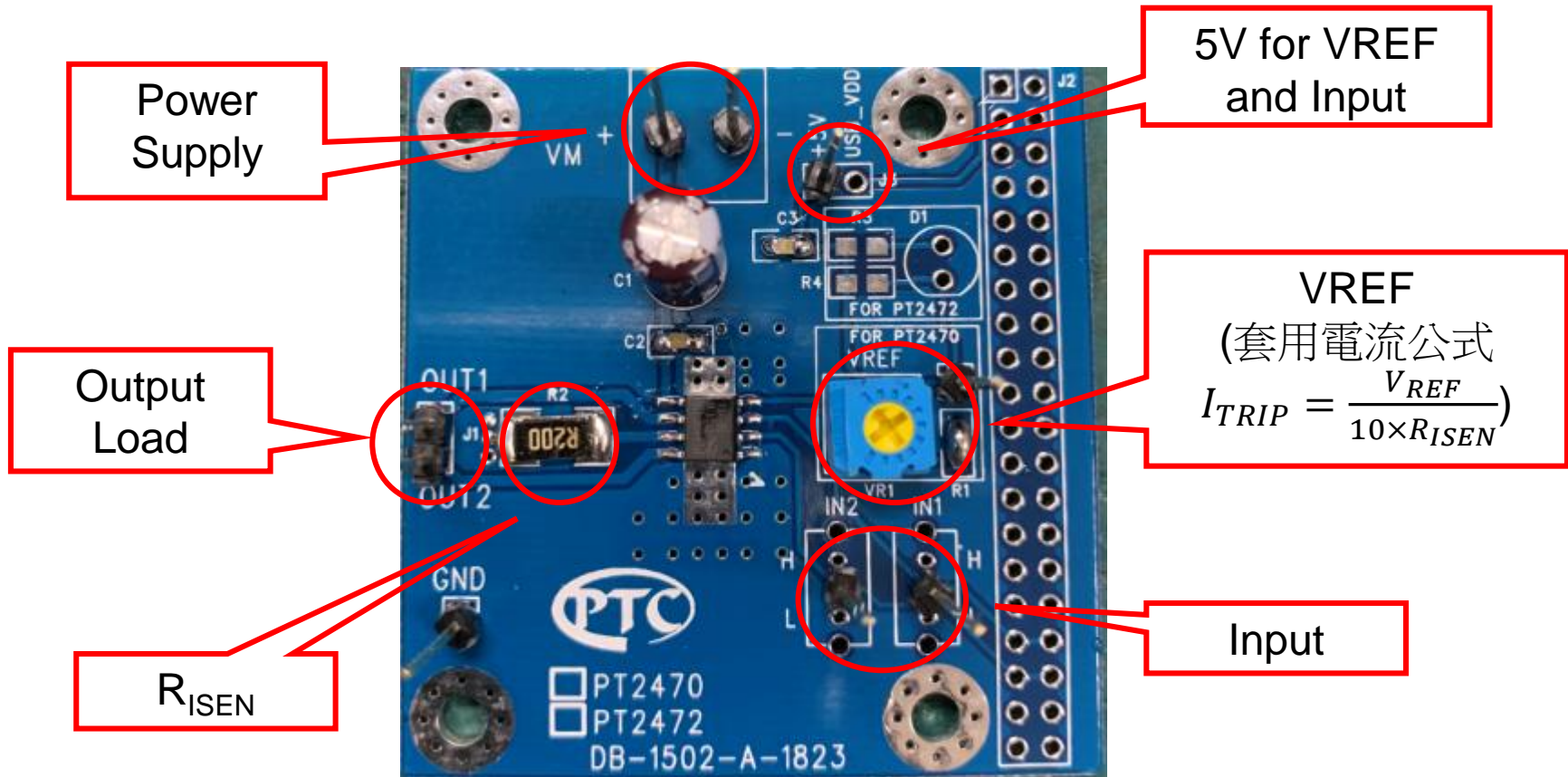
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PT2470 EV Board Outline



PT2470 EV Board I/O Port Description



IN1	IN2	OUT1	OUT2	DESCRIPTION
0	0	High-Z	High-Z	Coast; H-bridge disabled to High-Z (sleep entered after 1 ms)
0	1	L	H	Reverse (Current OUT2→Out1)
1	0	H	L	Forward (Current OUT1→Out2)
1	1	L	L	Brake; low side slow decay

PT2470 EV Board Circuit

